

10/526002

FORM PTO-1449	SERIAL NO. 10/526,002 Not yet assigned	CASE NO. 10/526,002
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE 02/25/2005 herewith	GROUP ART UNIT 2825 ***
(use several sheets if necessary)	APPLICANT(S): Wolfgang Günther et al.	

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER <small>Number-Kind Code (if known)</small>	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
W	A1	5,995,730	November 30, 1999	Blinne		

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER <small>Number-Kind Code (if known)</small>	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES OR NO

EXAMINER
INITIAL

OTHER ART - NON PATENT LITERATURE DOCUMENTS

(Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.

W	A2	Copy of International Examination Report from corresponding PCT patent application No. PCT/EP03/09548.
W	A3	R. Drechsler, <i>Equivalence Checking of Digital Circuits in an Industrial Environment</i> , IT+TI Informationstech, Tech. Info., Germany, pgs. 200-205, April 2001.
W	A4	Emmanuel Liégeois, <i>Speed Up ASIC Verification Using Formal Verification</i> , Electronique, No. 122, February 2002.
W	A5	Subash Chandar G., <i>Addressing Bottlenecks of Fully Synthesized Processor Cores Using Equivalence Checkers</i> , IEEE, pgs. 175-180, 2001.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.